

**PATENT NUMBER**

**O.I.P.E.**

**PATENT DATE**

SCANNED

QA

CONT/PRIOR  
F

CLASS  
710

**SUBCLASS**

ART UNIT  
2181

**EXAMINER**

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Output buffer circuit and control method therefor

PTO-2040  
12/89

## ORIGINAL

**CROSS REFERENCE(S)**

## CLASS

SUBCLASS

**CLASS**

**SUBCLASS (ONE SUBCLASS PER BLOCK)**

### INTERNATIONAL CLASSIFICATION

 Continued on Issue Slip Inside File Jacket

☐ **TERMINAL  
DISCLAIMER**

## DRAWINGS

**Sheets Drwg.**

Figs. Drwg.

Print Fig.

**CLAIMS ALLOWED**

### Total Claims

**Print Claim for O.G.**

☐ The term of this patent subsequent to \_\_\_\_\_ (date) has been disclaimed.

☐ The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. \_\_\_\_\_

☐ The terminal \_\_\_\_ months of this patent have been disclaimed.

**NOTICE OF ALLOWANCE MAILED**

## ISSUE FEE

**Amount Due**

Date Paid

**ISSUE BATCH NUMBER**

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(Rev. 6/99)

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(FACE)